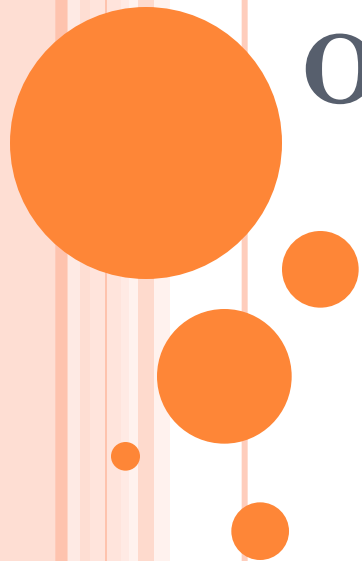


ТЕХНИЧЕСКИ УНИВЕРСИТЕТ – СОФИЯ
КАТЕДРА “КОМПЮТЪРНИ СИСТЕМИ”

ВИСОКОПРОИЗВОДИТЕЛНИ КОМПЮТЪРНИ СИСТЕМИ

ДИНАМИЧНИ ХАРАКТЕРИСТИКИ
НА N-RADIX КОМУТАТОР В СРЕДА
OMNET++

ДЕСИСЛАВА ИВАНОВА



КОНФИГУРИРАНЕ НА ЗАДАНИЕТО

o n-radix_switch.ini

The screenshot shows the OMNeT++ IDE interface. The main window displays the configuration file `n-radix_switch.ini` with the following content:

```
[General]
#record-eventlog = true
#sim-time-limit = 100h

#cpu-time-limit = 120 s
#debug-on-errors = true

**.scalar-recording = true
output-scalar-file = ${resultdir}/${configname}-${runnumber}.sca

cmdenv-interactive = false
cmdenv-express-mode = true

network = n_radix_switch

# this parameter specifies the switch radix and also number of receiving/sending hosts
n-radix-switch.n = 8
i *.switch[*].BufferLength = 16

#
*.switch[*].RoutingAlgorithm = "SourceTag"

# total number of packets sent by each host
*.host[*].PacketsToSend = 1000

# packet size (in flits)
*.host[*].PacketSize = 8
# големина на пакета

#
*.host[*].TrafficPattern = "Uniform"

# specifies offered load from 0 to 100%
*.host[*].PacketRate = (${10..100 step 10})

# Flit size (in bits)
i *.host[*].FlitSize = 16
*.switch[*].SwitchClock = 8 ns
```

Annotations in the image:

- A red circle highlights the tab `n-radix_switch.ini` in the top right.
- A red circle highlights the line `n-radix-switch.n = 8` with the annotation "брой портове (радикс)".
- A red circle highlights the line `*.switch[*].BufferLength = 16`.
- A red circle highlights the line `*.host[*].PacketSize = 8` with the annotation "големина на пакета".



Simulation - n_radix_switch/src/n_radix_switch.ini - OMNeT++ IDE

File Edit Source Navigate Search Project Run SVN Window Help

Run Ctrl+F11
Debug F11

Run History
Run As
Run Configurations...
Debug History
Debug As
Debug Configurations...
External Tools

Project Explorer

- src 93
 - Host 90
 - results 77
 - latency.xls
 - latency-1.csv
 - Uniform-0.sca
 - Uniform-1.sca
 - Uniform-2.sca
 - Uniform-3.sca
 - Uniform-4.sca
 - Uniform-5.sca
 - Uniform-6.sca
 - Uniform-7.sca
 - Uniform-8.sca
 - Uniform-9.sca
 - Switch 84
 - Credit_m.cc
 - Credit_m.h
 - Flit_m.cc
 - Flit_m.h
 - n_radix_switch.exe - [x86/le]
 - channels.ned 92
 - Credit.msg 32
 - Flit.msg 31
 - Host.ned 89
 - k-ary_n-mesh.ini 93

```
*.scalar-recording = true
output-scalar-file = ${resultdir}/${configname}-${runnumber}.sca

cmdenv-interactive = false
cmdenv-express-mode = true

network = n_radix_switch

# this parameter specifies the switch radix and also number of recs
# n radix switch.n = (${4..8 step 1})
n_radix_switch.n = 8

i
*.switch[*].*.BufferLength = 16

#
*.switch[*].*.RoutingAlgorithm = "SourceTag"

# total number of packets sent by each host
*.host[*].PacketsToSend = 1000

# packet size (in flits)
*.host[*].PacketSize = 8

#
*.host[*].TrafficPattern = "Uniform"

# specifies offered load form 0 to 100%
*.host[*].PacketRate = (${10..100 step 10})

# Flit size (in bits)
i
*.host[*].FlitSize = 16
*.switch[*].*.SwitchClock = 0 ns
```

Properties Outline

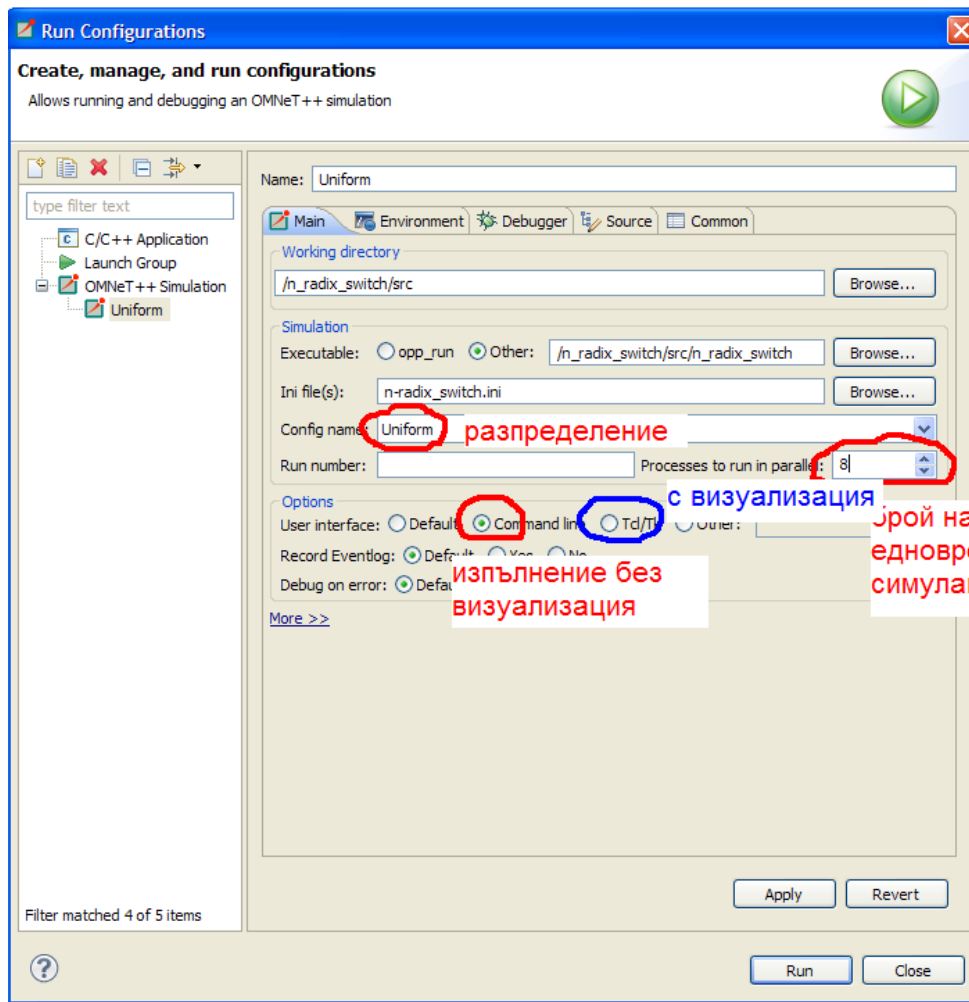
n_radix_switch/sr...-radix_switch.ini

Resource	Property	Value
Info	deriv	false
	edit	true
	last	20 mарт 2011...
	link	false
	loc	C:\omnetpp-4...
	nam	n-radix_switch.ini
	path	/n_radix_switc...

Form Source



ЗАДАВАНЕ НА ИЗПЪЛНЕНИЕТО НА СИМУЛАЦИЯТА



СТАРТИРАНЕ И РЕЗУЛТАТИ

The screenshot displays the OMNeT++ IDE interface. The Project Explorer on the left shows a directory structure with a folder named 'results' containing several 'Uniform-*.sca' files, which are circled in red. The main editor shows the configuration file 'n_radix_switch.ini' with various simulation parameters. A red circle highlights the line 'n_radix switch.n = 8'. The Console window at the bottom shows the execution output, with a red circle around the data for hosts 4, 5, and 6. A red arrow points from the 'results' folder to the console output.

результати (скаларни файлове)

```
[General]
#record-eventlog = true
#sim-time-limit = 100h

#cpu-time-limit = 120 s
#debug-on-errors = true

**.scalar-recording = true
output-scalar-file = ${resultdir}/${configname}-${runnumber}.sca

cmdenv-interactive = false
cmdenv-express-mode = true
network = n_radix_switch

# this parameter specifies the switch radix and also number of receiving/sending hosts
n_radix switch.n = 8

i
*.switch[*].*.BufferLength = 16

#
*.switch[*].*.RoutingAlgorithm = "SourceTag"

# total number of packets sent by each host
*.host[*].PacketsToSend = 1000

# packet size (in flits)
*.host[*].PacketSize = 8

#
*.host[*].TrafficPattern = "Uniform"

# specifies offered load form 0 to 100%
*.host[*].PacketRate = (${10..100 step 10})

# Flit size (in bits)
i
*.host[*].FlitSize = 16
*.switch[*].*.SwitchClock = 8 ns
```

изпълнения на конзолата

```
<terminated> Uniform [OMNeT++ Simulation] src/n_radix_switch.exe (41-4-4 7:32 - run #0)
host[4] total_latency: 0.00015818473
host[5] total_packets_sent: 1000
host[5] total_packets_received: 1022
host[5] total_latency: 0.000160108345
host[6] total_packets_sent: 1000
host[6] total_packets_received: 993
host[6] total_latency: 0.000155996077
```

СТАРТИРАНЕ В ГРАФИЧНА СРЕДА

OMNeT++/Tkenv - n_radix_switch

File Edit Simulate Trace Inspect View Options Help

Run #0: n_radix_switch

Msgs scheduled: 16

Ev/sec: n/a

15 credits...

15 credits

credit...

credit

Event #1

Msgs created: 32

Simsec/sec: n/a

T=0

Uniform: GetDestination n=1, k=8

(n_radix_switch) n_radix_switch

[n_radix_switch] n_radix_switch (id=1) (ptr05A05D70)

n_radix_switch

host[1]

host[7]

host[5]

host[3]

host[0]

host[4]

host[2]

host[6]

double click

Initializing module n_radix_switch.switch[0].reg[0].stage 0

Initializing module n_radix_switch.switch[0].reg[1].stage 0

Initializing module n_radix_switch.switch[0].reg[2].stage 0

Initializing module n_radix_switch.switch[0].reg[3].stage 0

Initializing module n_radix_switch.switch[0].reg[4].stage 0

Initializing module n_radix_switch.switch[0].reg[5].stage 0

Initializing module n_radix_switch.switch[0].reg[6].stage 0

Initializing module n_radix_switch.switch[0].reg[7].stage 0

Initializing module n_radix_switch.switch[0].buffer[0].stage 0

Initializing module n_radix_switch.switch[0].buffer[1].stage 0

Initializing module n_radix_switch.switch[0].buffer[2].stage 0

Initializing module n_radix_switch.switch[0].buffer[3].stage 0

Initializing module n_radix_switch.switch[0].buffer[4].stage 0

Initializing module n_radix_switch.switch[0].buffer[5].stage 0

Initializing module n_radix_switch.switch[0].buffer[6].stage 0

Initializing module n_radix_switch.switch[0].buffer[7].stage 0

Initializing module n_radix_switch.switch[0].mux[0].stage 0

Initializing module n_radix_switch.switch[0].mux[1].stage 0

Initializing module n_radix_switch.switch[0].mux[2].stage 0

Initializing module n_radix_switch.switch[0].mux[3].stage 0

Initializing module n_radix_switch.switch[0].mux[4].stage 0

Initializing module n_radix_switch.switch[0].mux[5].stage 0

(Switch) n_radix_switch.switch[0]

[Switch] n_radix_switch.switch[0] (id=10) (ptr05A0C6C0) Zoom: 0.39x

n_radix_switch

req[0]

in_ct[0]

req[1]

in_ct[1]

req[2]

in_ct[2]

req[3]

in_ct[3]

req[4]

in_ct[4]

req[5]

in_ct[5]

req[6]

in_ct[6]

req[7]

in_ct[7]

buffer[0]

buffer[1]

buffer[2]

buffer[3]

buffer[4]

buffer[5]

buffer[6]

buffer[7]

mux[0]

mux[1]

mux[2]

mux[3]

mux[4]

mux[5]

mux[6]

mux[7]

port[0]

port[1]

port[2]

port[3]

port[4]

port[5]

port[6]

port[7]



ИЗВЛИЧАНЕ НА СТАТИСТИКИ

Simulation - n_radix_switch/src/Uniform.anf - OMNeT++ IDE

Project Explorer: src 93, Host 90, results 77, latency.xls, Uniform-1.sca, Uniform-2.sca, Uniform-6.sca, Uniform-7.sca, Uniform-8.sca, Uniform-9.sca, Switch 84, Credit_m.cc, Credit_m.h, Flit_m.cc, Flit_m.h, n_radix_switch.exe - [x86/le], channels.ned 92, Credit.msg 32, Flit.msg 31, Host.ned 89, k-ary_n-mesh.ini 93, k-ary_n-mesh.ned 91

Host.cc | Host.h | RoutingAlgorithm.cc | SourceTag.cc | k-ary_n-mesh.ini | k-ary_n-mesh.ned | n_radix_switch.ned | Uniform.anf | n_radix_switch.ini

Browse Data

here you can see all data that come from the files specified in the configuration file

All (0 / 0) | Vectors (0 / 0) | Scalars (25200 / 25200)

double click

таб със скаларните резултати

Folder	File name	Config name	Run id	Module	Name	Value	
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Throughput:max 0.0	
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Throughput:min 0.0	
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Throughput:sqr...	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Throughput:sum	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Throughput:std...	NaN
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Throughput:mean	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Throughput:count	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Latency:max	3.89768E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Latency:min	1.506E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Latency:sqsum	2.6486860476679E-11
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Latency:sum	1.5506588E-4
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Latency:stddev	3.4033006865789E-8
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Latency:mean	1.6374433474129E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[66]	Latency:count	947.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Throughput:max	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Throughput:min	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Throughput:sqr...	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Throughput:sum	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Throughput:std...	NaN
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Throughput:mean	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Throughput:count	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Latency:max	3.77597E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Latency:min	1.506E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Latency:sqsum	2.7684241000143E-11
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Latency:sum	1.60834731E-4
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Latency:stddev	3.5173231186642E-8
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Latency:mean	1.6462101432958E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[65]	Latency:count	977.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Throughput:max	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Throughput:min	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Throughput:sqr...	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Throughput:sum	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Throughput:std...	NaN
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Throughput:mean	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Throughput:count	0.0
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Latency:max	4.32957E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Latency:min	1.506E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Latency:sqsum	2.8804991093212E-11
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	Uniform-1-20110404-06:21:02-3768	n_radix_switch.host[67]	Latency:sum	1.64513238E-4

Input: Browse Data | Datasets

```
<terminated> Uniform (1) [OMNeT++ Simulation] src/n_radix_switch.exe (11-4-4 7:37 - run #0)
undisposed object: (cMessage) n_radix_switch.host.timer -- check module destructor
undisposed object: (cMessage) n_radix_switch.host.timer -- check module destructor
undisposed object: (cMessage) n_radix_switch.switch[0].buffer.timer -- check module destructor
undisposed object: (cMessage) n_radix_switch.switch[0].buffer.timer -- check module destructor
```



СОРТИРАНЕ И КОПИРАНЕ НА РЕЗУЛТАТИТЕ

The screenshot shows the OMNeT++ Simulation interface. The 'Browse Data' window is open, displaying a table of simulation results. The table has columns for Folder, File name, Config name, runID, module, and Name. The 'runID filter' dropdown is set to 'Latency:mean'. The 'module filter' dropdown is also set to 'Latency:mean'. The table contains 31 rows of data, each representing a different simulation run. The console at the bottom shows the output of the simulation, including the command 'src/n_radix_switch.exe (11-4-4 7:37 - run #0)' and several 'undisposed object' messages.

избот на филтър

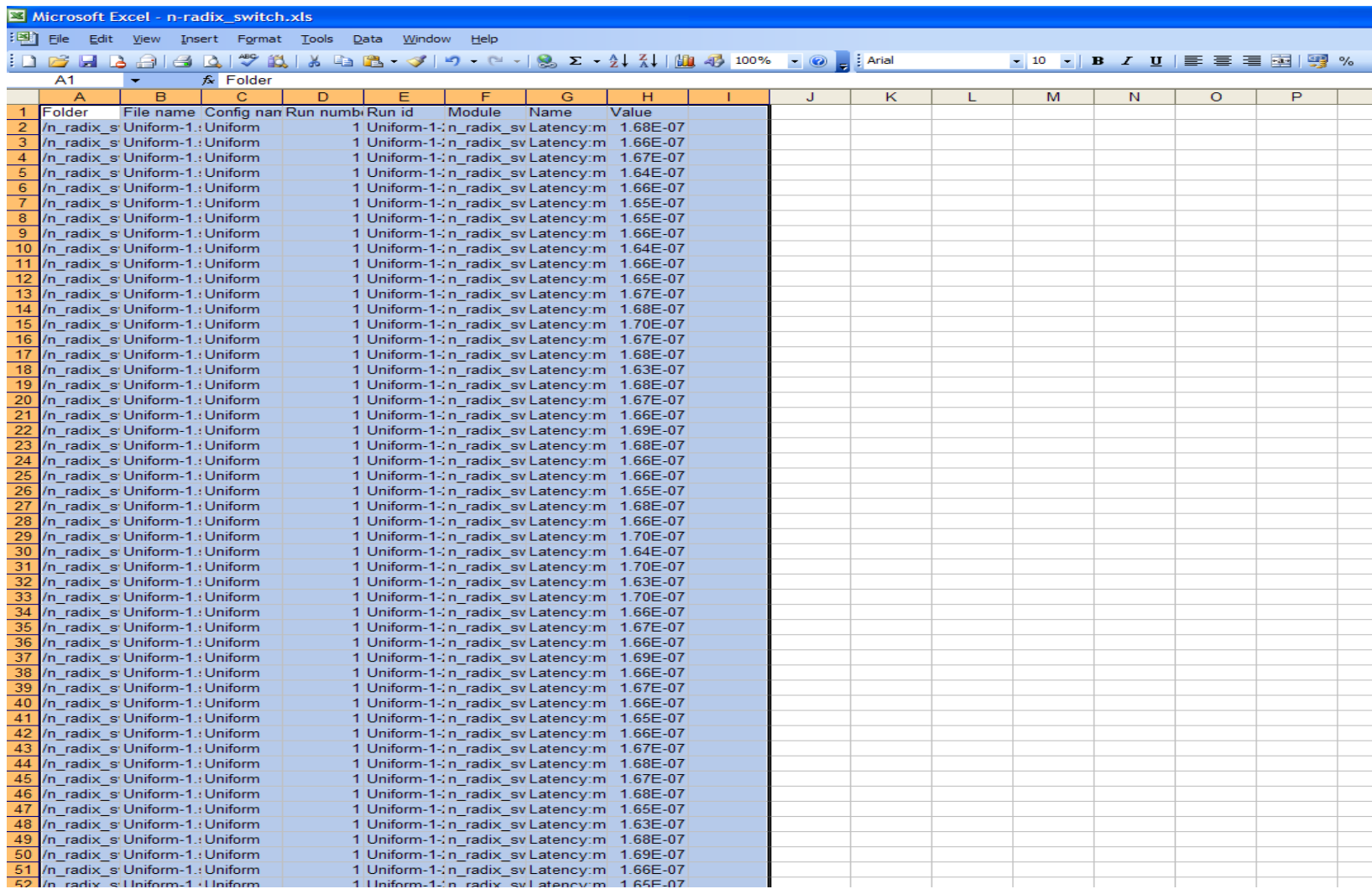
сортитране по номер на симулацията - задава приложениата товар

с Ctrl+A селектираме всичко с десен бутон на мишката изпираме "copy to clipboard"

Folder	File name	Config name	runID	module	Name	Value
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[44]	Latency:mean	1.6897192028254E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[43]	Latency:mean	1.6553225947577E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[42]	Latency:mean	1.6602122040816E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[41]	Latency:mean	1.6684225177665E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[40]	Latency:mean	1.6577955336788E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[39]	Latency:mean	1.6822354959514E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[38]	Latency:mean	1.6695073405299E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[25]	Latency:mean	1.6599085329341E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[26]	Latency:mean	1.644659866251E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[27]	Latency:mean	1.666668997975E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[28]	Latency:mean	1.6514848551448E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[29]	Latency:mean	1.6580087201528E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[30]	Latency:mean	1.6604794832827E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[31]	Latency:mean	1.6869250759878E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[32]	Latency:mean	1.705145015045E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[33]	Latency:mean	1.6551224771574E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[34]	Latency:mean	1.669977270935E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[35]	Latency:mean	1.6648162033195E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[36]	Latency:mean	1.6687274065599E-7
/n_radix_switch/src/results/	Uniform-1.sca	Uniform	1	n_radix_switch.host[37]	Latency:mean	1.6897681406551E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[35]	Latency:mean	1.7948883E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[36]	Latency:mean	1.8190523099134E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[34]	Latency:mean	1.7832506808943E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[33]	Latency:mean	1.774433207739E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[32]	Latency:mean	1.82052024637E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[31]	Latency:mean	1.7467321515151E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[30]	Latency:mean	1.8055791321118E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[29]	Latency:mean	1.8010571498054E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[28]	Latency:mean	1.7873683E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[27]	Latency:mean	1.7573725373134E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[26]	Latency:mean	1.8277187487685E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[25]	Latency:mean	1.7752947729566E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[24]	Latency:mean	1.8178653534401E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[23]	Latency:mean	1.8338200391773E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[22]	Latency:mean	1.756553024236E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[21]	Latency:mean	1.806616615087E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[20]	Latency:mean	1.7701947584789E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[19]	Latency:mean	1.7808826598985E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[18]	Latency:mean	1.8133382217573E-7
/n_radix_switch/src/results/	Uniform-2.sca	Uniform	2	n_radix_switch.host[17]	Latency:mean	1.7044300487805E-7

```
<terminated> Uniform (1) [OMNeT++ Simulation] src/n_radix_switch.exe (11-4-4 7:37 - run #0)
undisposed object: (cMessage) n_radix_switch.host.timer -- check module destructor
undisposed object: (cMessage) n_radix_switch.host.timer -- check module destructor
undisposed object: (cMessage) n_radix_switch.switch[0].buffer.timer -- check module destructor
undisposed object: (cMessage) n_radix_switch.switch[0].buffer.timer -- check module destructor
undisposed object: (cMessage) n_radix_switch.switch[0].buffer.timer -- check module destructor
```


ПРЕХВЪРЛЯНЕ В MS EXCEL



Microsoft Excel - n-radix_switch.xls

File Edit View Insert Format Tools Data Window Help

100% Arial

	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q
	Folder	File name	Config nan	Run numb	Run id	Module	Name	Value									
1	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.68E-07									
2	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
3	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.67E-07									
4	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.64E-07									
5	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
6	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.65E-07									
7	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.65E-07									
8	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
9	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.64E-07									
10	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
11	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.65E-07									
12	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.67E-07									
13	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.68E-07									
14	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.70E-07									
15	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.67E-07									
16	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.68E-07									
17	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.63E-07									
18	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.68E-07									
19	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.67E-07									
20	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
21	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.69E-07									
22	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.68E-07									
23	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
24	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
25	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.65E-07									
26	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.68E-07									
27	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
28	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
29	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.70E-07									
30	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.64E-07									
31	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.70E-07									
32	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.63E-07									
33	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.70E-07									
34	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
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36	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
37	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.69E-07									
38	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
39	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.67E-07									
40	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
41	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.65E-07									
42	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
43	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.67E-07									
44	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.68E-07									
45	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.67E-07									
46	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.68E-07									
47	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.65E-07									
48	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.63E-07									
49	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.68E-07									
50	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.69E-07									
51	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.66E-07									
52	/n_radix_s	Uniform-1: Uniform		1	Uniform-1:n_radix_sv	Latency:m		1.65E-07									

